



## LAN91C111 Rev C

### *Revision Impact Notice*

#### Effect of ID Register Value Change on Software

Rev. 1.0

November 29, 2005

Standard Microsystems is a registered trademark of Standard Microsystems Corporation, and SMSC is a trademark of Standard Microsystems Corporation. Product names and company names are the trademarks of their respective holders. Circuit diagrams utilizing SMSC products are included as a means of illustrating typical applications; consequently complete information sufficient for construction purposes is not necessarily given. Although the information has been checked and is believed to be accurate, no responsibility is assumed for inaccuracies. SMSC reserves the right to make changes to specifications and product descriptions at any time without notice. Contact your local SMSC sales office to obtain the latest specifications before placing your product order. The provision of this information does not convey to the purchaser of the semiconductor devices described any licenses under the patent rights of SMSC or others. All sales are expressly conditional on your agreement to the terms and conditions of the most recently dated version of SMSC's standard Terms of Sale Agreement dated before the date of your order (the "Terms of Sale Agreement"). The product may contain design defects or errors known as anomalies, which may cause the product's functions to deviate from published specifications. Anomaly sheets are available upon request. SMSC products are not designed, intended, authorized or warranted for use in any life support or other application where product failure could cause or contribute to personal injury or severe property damage. Any and all such uses without prior written approval of an Officer of SMSC and further testing and/or modification will be fully at the risk of the customer. Copies of this document or other SMSC literature, as well as the Terms of Sale Agreement, may be obtained by visiting SMSC's website at <http://www.smsc.com>.

**SMSC DISCLAIMS AND EXCLUDES ANY AND ALL WARRANTIES, INCLUDING WITHOUT LIMITATION ANY AND ALL IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE, AND AGAINST INFRINGEMENT, AND ANY AND ALL WARRANTIES ARISING FROM ANY COURSE OF DEALING OR USAGE OF TRADE.**

IN NO EVENT SHALL SMSC BE LIABLE FOR ANY DIRECT, INCIDENTAL, INDIRECT, SPECIAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES, OR FOR LOST DATA, PROFITS, SAVINGS OR REVENUES OF ANY KIND; REGARDLESS OF THE FORM OF ACTION, WHETHER BASED ON CONTRACT, TORT, NEGLIGENCE OF SMSC OR OTHERS, STRICT LIABILITY, BREACH OF WARRANTY, OR OTHERWISE; WHETHER OR NOT ANY REMEDY IS HELD TO HAVE FAILED OF ITS ESSENTIAL PURPOSE; AND WHETHER OR NOT SMSC HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

## 1. Issue

The LAN91C111 Rev. C part has a different revision register value than that of its Rev. B predecessor. This is by design, as the revision of the device has changed.

For the *Rev. B* device, the value of the Revision Register (Bank 3) is **91h** and appears as follows:

OFFSET A	NAME REVISION REGISTER	TYPE READ ONLY	SYMBOL REV
HIGH BYTE			
	0	0	1
	1	1	0
	0	0	1
	1	1	0
LOW BYTE	CHIP	REV	
	1	0	0
	0	0	1
	0	0	0
	1	0	1

For the *Rev. C* device, the value of the Revision Register (Bank 3) is **92h** and appears as follows:

OFFSET A	NAME REVISION REGISTER	TYPE READ ONLY	SYMBOL REV
HIGH BYTE			
	0	0	1
	1	1	0
	0	0	1
	1	1	0
LOW BYTE	CHIP	REV	
	1	0	1
	0	0	0
	0	0	1
	1	0	0

## 2. Impact

Some manufacturing software may rely on the value of the ID register for board validation. Also, a software device driver may use the ID register for conditional branching. This is the case when a driver performs a different operation, based on the device revision, due to an anomaly that may have been present in an earlier revision, but was fixed in a subsequent stepping.

An example of this is the Rev. A (ID register of **90h**) ODD\_BYTE anomaly, which was fixed in Rev. B and all subsequent revisions. The software workaround should only be executed for Rev. A devices, not Rev. B or C devices.