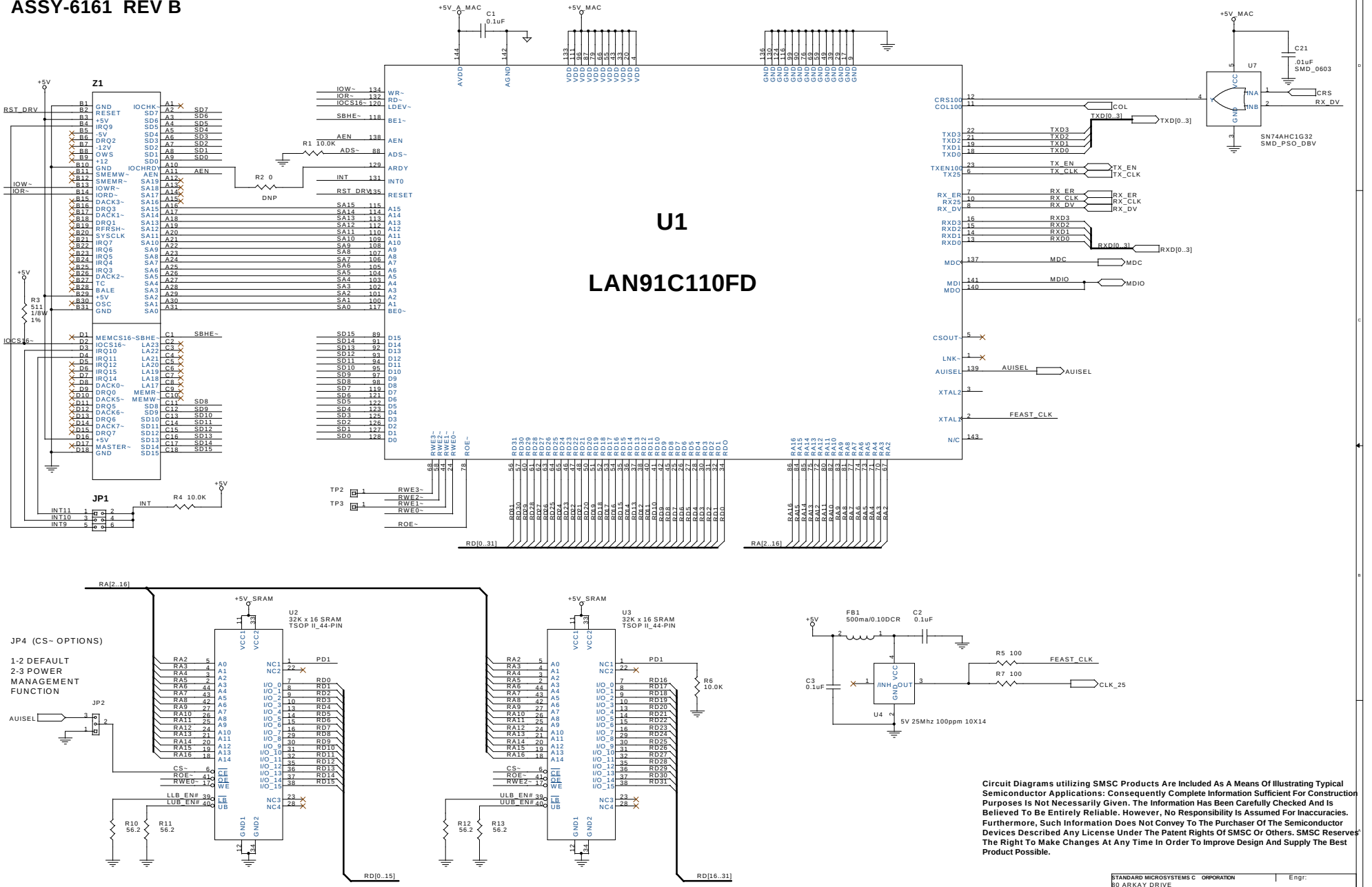
SMSC LAN91C110 LOW COST ISA MAC REFERENCE DESIGN
ASSY-6161 REV B



Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

STANDARD MICROSYSTEMS C ORPORATION
80 ARKAY DRIVE
PO Box 18047
HAUPPAUGE NY, 11788-8847

Engr:

Title: LAN110_183 10/100 $A MAC LOW COST EVAL BOARD

Size	Document Number	Rev
	6161	B

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SMSC LAN91C110 LOW COST ISA MAC REFERENCE DESIGN ASSY-6161

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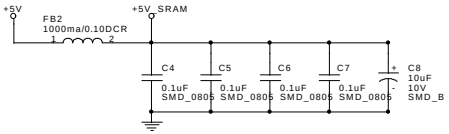
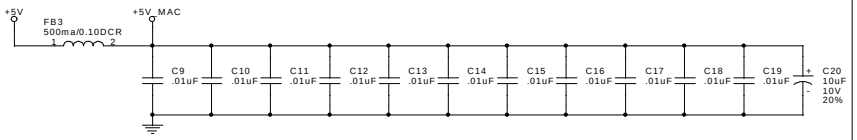
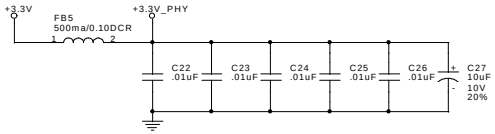


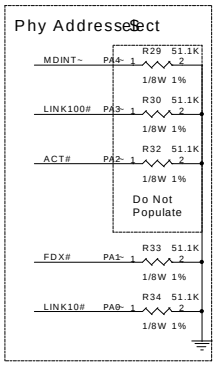




NOTE: Decoupling caps should be placed on underside of board as close to device as possible

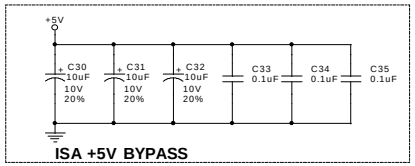
Notes:

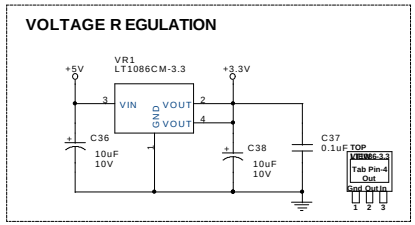
1. All MII signal traces to be constructed with typical 68 ohm impedance.
2. PCB Construction: 4 layer, 1 oz. copper, .015" dielectric.
3. MII trace width: .0130".
4. Ethernet trace width: .0100" with .0400" spacing.

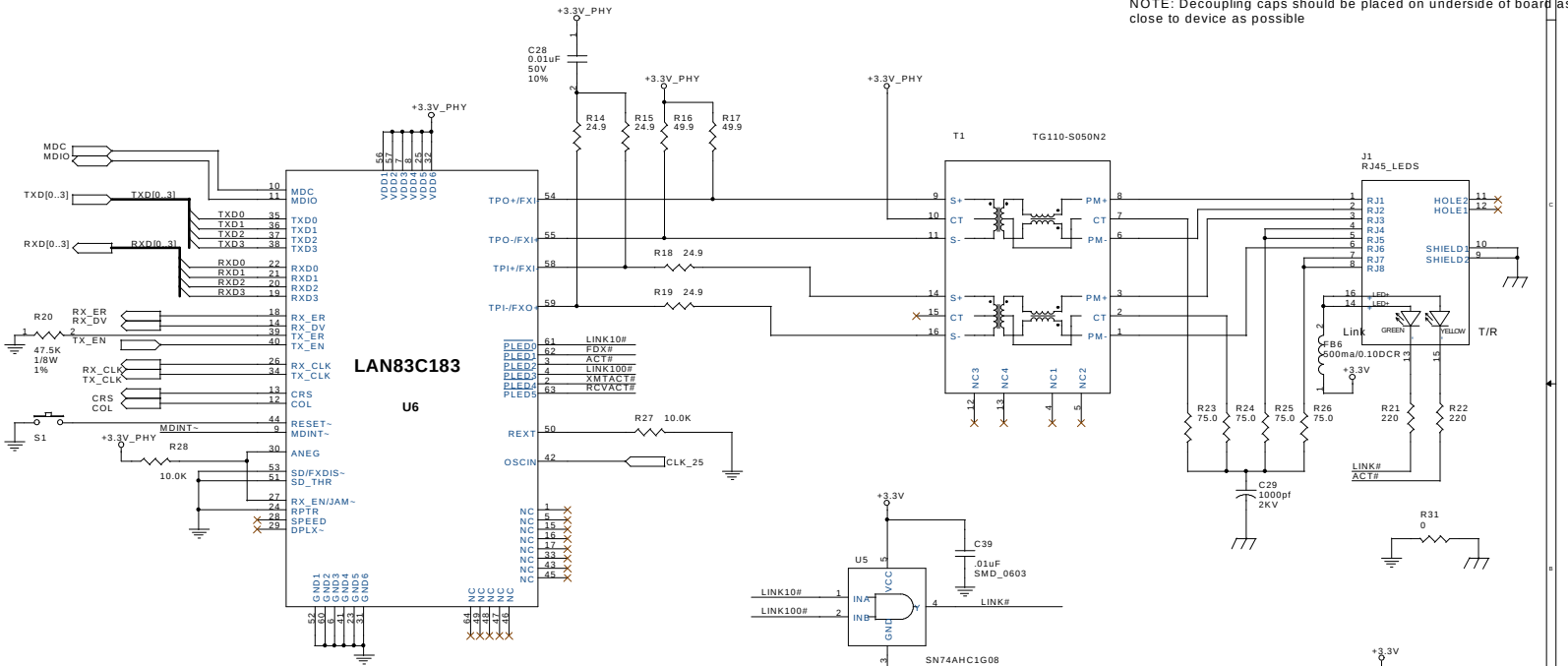


The LAN83C183 will power-up with the following defaults:

1) Autonegotiate
2) Phy Address 03







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