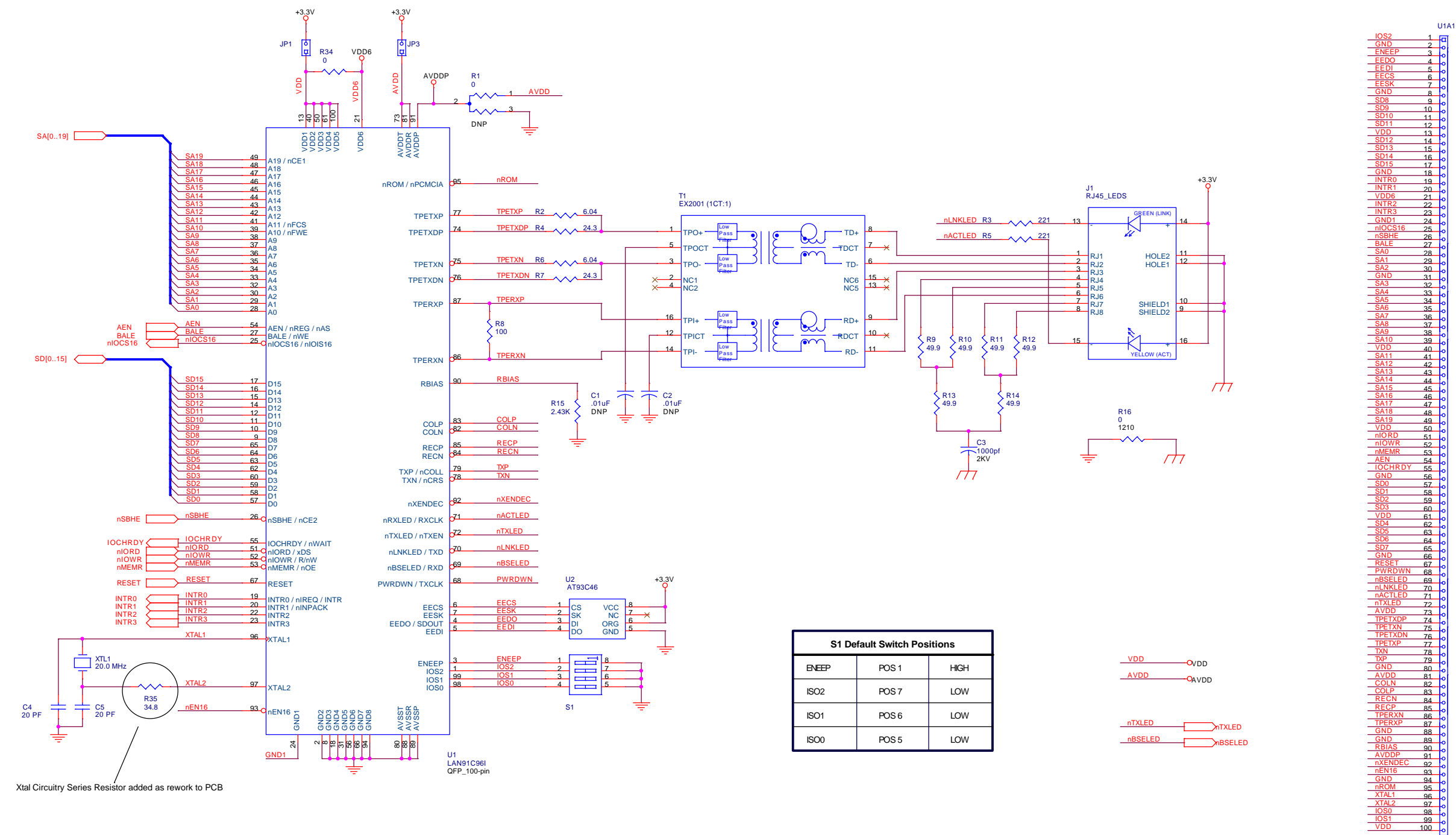


SMSC LAN91C96I +3.3V Evaluation Design

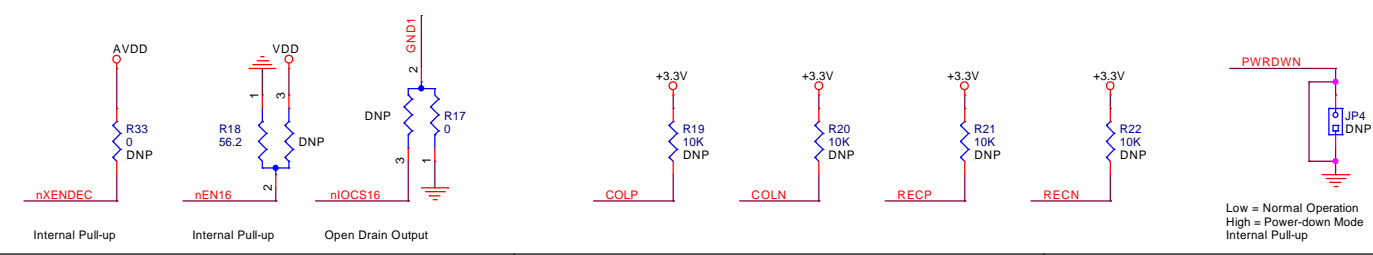
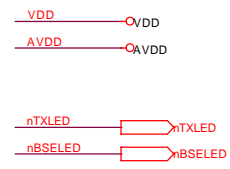
Assembly No. 6254 Revision A3

Rev	ECN Description	Date
A	Release	5-24-02
A1	Change value of R2, R4, R6 & R7	5-30-02
A2	Change value of R15	6-4-02
A3	Added Xtal Circuit Series resistor as rework to board	9-10-03



Pin	Signal
1	IOS2
2	GND
3	ENEPP
4	EEDO
5	EEDI
6	EESK
7	EESK
8	GND
9	SD8
10	SD9
11	SD10
12	SD11
13	VDD
14	SD12
15	SD13
16	SD14
17	SD15
18	GND
19	INTR0
20	INTR1
21	VDD6
22	INTR2
23	INTR3
24	GND1
25	nIOCS16
26	nSBHE
27	BALE
28	SA0
29	SA1
30	SA2
31	GND
32	SA3
33	SA4
34	SA5
35	SA6
36	SA7
37	SA8
38	SA9
39	SA10
40	VDD
41	SA11
42	SA12
43	SA13
44	SA14
45	SA15
46	SA16
47	SA17
48	SA18
49	SA19
50	VDD
51	nIORD
52	nMEMR
53	nMEMR
54	AEN
55	IOCHRDY
56	GND
57	SD0
58	SD1
59	SD2
60	SD3
61	VDD
62	SD4
63	SD5
64	SD6
65	SD7
66	GND
67	RESET
68	PWRDWN
69	PWRDWN
70	nLNKLED
71	nACTLED
72	nTXLED
73	AVDD
74	TPETXDP
75	TPETXN
76	TPETXDN
77	TPETXP
78	TXN
79	TXP
80	GND
81	AVDD
82	COLP
83	COLN
84	RECN
85	RECP
86	TPERXN
87	TPERXP
88	GND
89	GND
90	RBIAS
91	AVDDP
92	nXENDEC
93	nEN16
94	GND
95	nROM
96	XTAL1
97	XTAL2
98	IOS0
99	IOS1
100	VDD

Switch	Position	Mode
ENEPP	POS 1	HIGH
ISO2	POS 7	LOW
ISO1	POS 6	LOW
ISO0	POS 5	LOW



Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

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STANDARD MICROSYSTEMS CORPORATION

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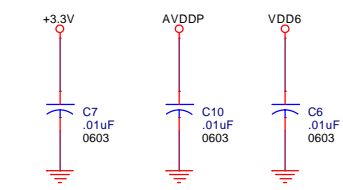
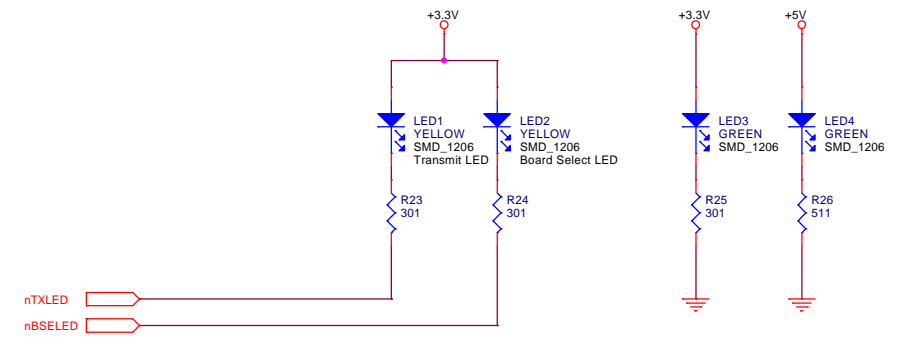
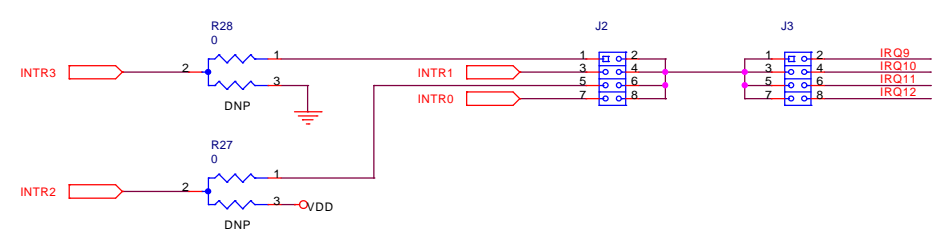
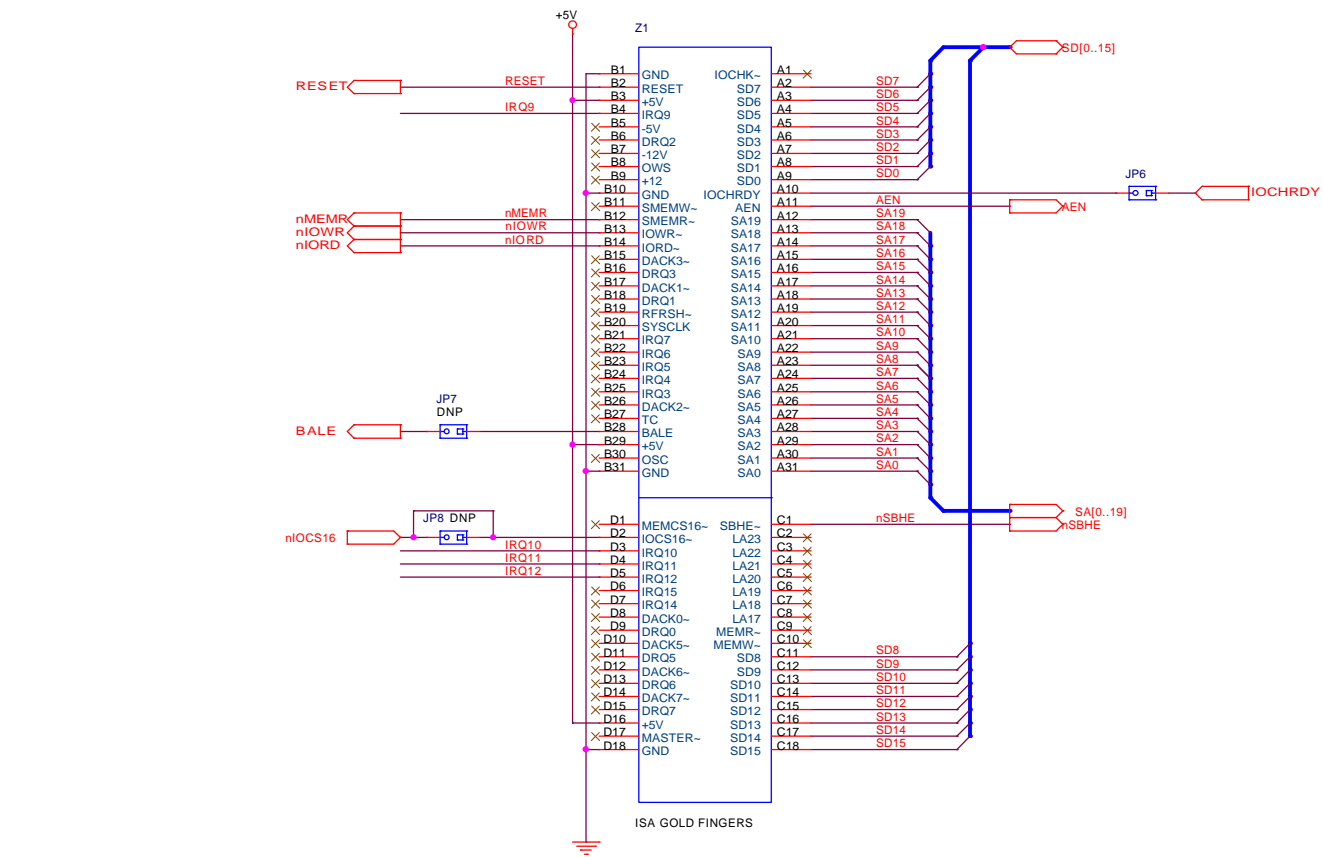
File: LAN91C96I +3.3V Operation

Size C	Engineer	Assembly No. 6254	Rev A3
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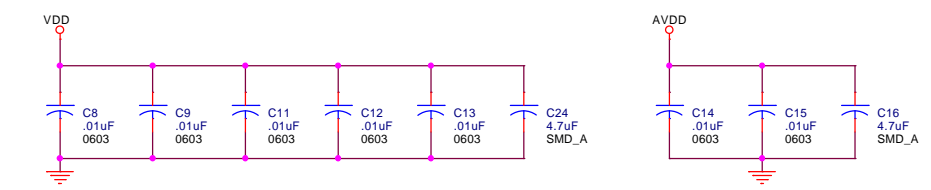
Date: Friday, March 05, 2004 | Sheet 1 of 2

SMSC LAN91C96I +3.3V Evaluation Design

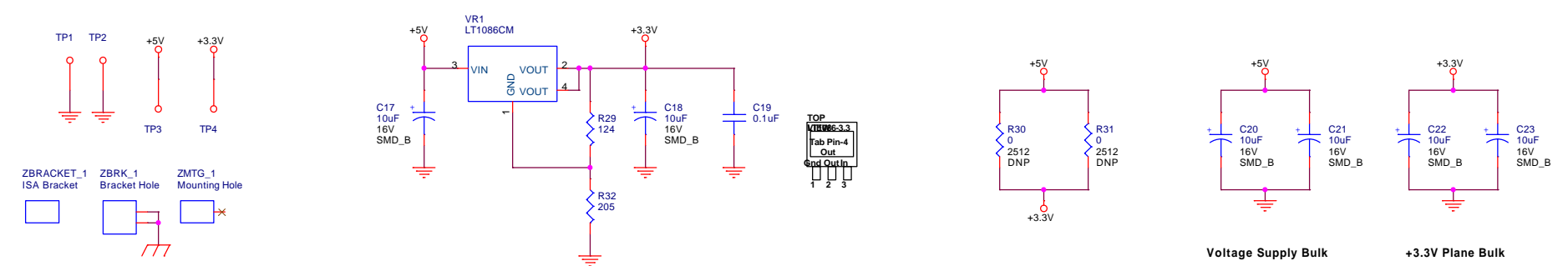
Assembly No. 6254 Revision A3



Decoupling Capacitors



+3.3V VOLTAGE REGULATION



Notes:

1. Daughter Board Footprint to accept both Assy No. 6072 (100-pin TQFP and Assy No. 6087 (100-pin QFP)
2. There are (5) 3-position jumper options on the board. All (5) default to the 1-2 position indicated with the band demarcation.
3. All components, unless otherwise indicated, are SMD_0805 size.

		80 Arky Drive PO Box 18047 Hauppauge, NY 11788-8847	
		LAN91C96I +3.3V Operation	
Size	Engineer	Assembly No.	Rev
C		6254	A3
Date:	Friday, March 05, 2004	Sheet	2 of 2