


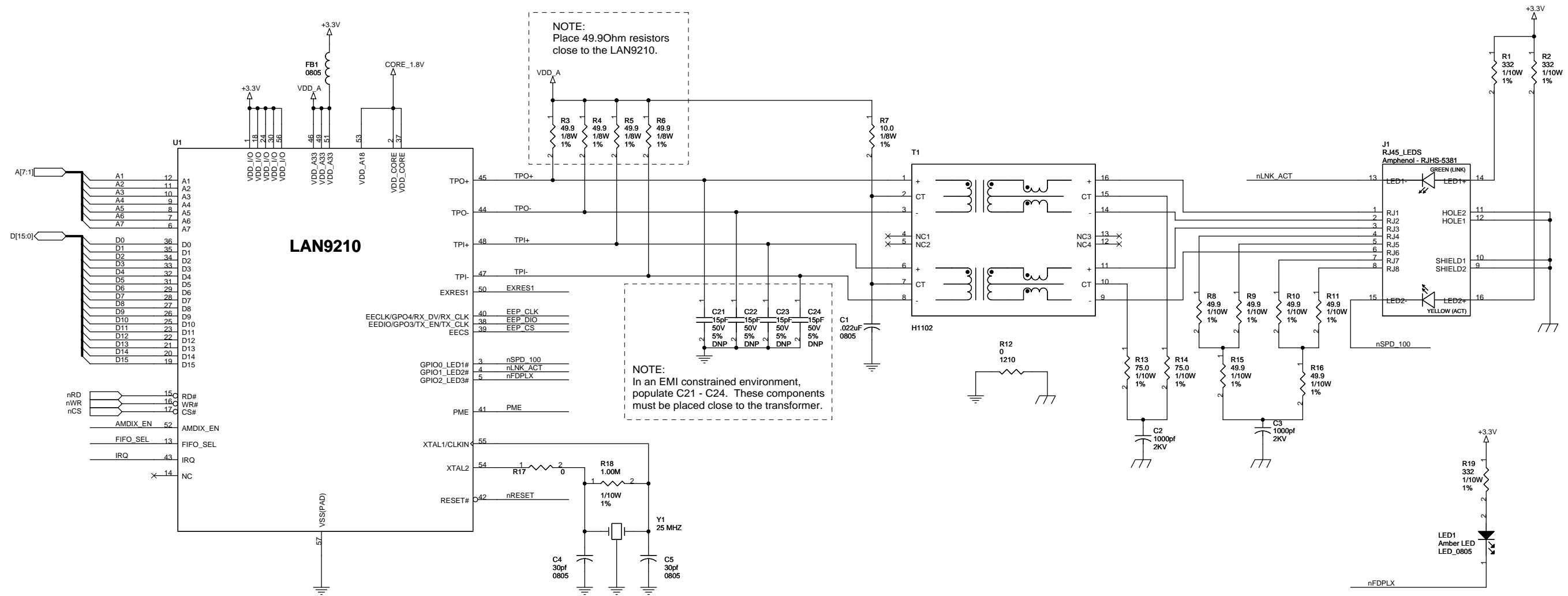
# LAN9210 Reference Design

Revision 0.6

Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

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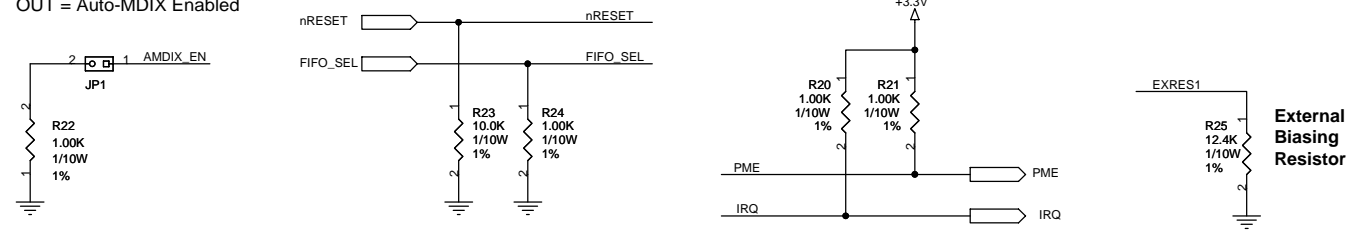
			
Title <b>LAN9210 Reference Design</b>			
Size C	Engineer S. Wynhurst	Assembly No.	Rev 0.6
Date: Thursday, January 03, 2008		Sheet 1 of 2	



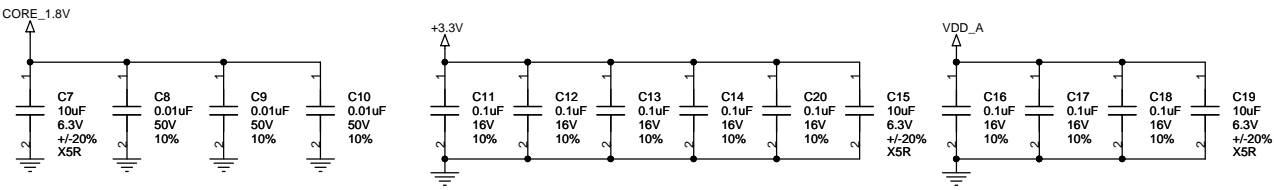
NOTE:  
Place 49.90hm resistors  
close to the LAN9210.

NOTE:  
In an EMI constrained environment,  
populate C21 - C24. These components  
must be placed close to the transformer.

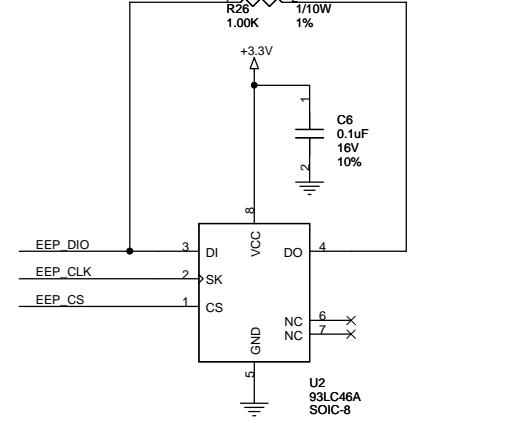
NOTE:  
IN = Auto-MDIX Disabled  
OUT = Auto-MDIX Enabled



### Decoupling Capacitors



### MAC EEPROM (Optional)



<b>SMSC</b> SUCCESS BY DESIGN			
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