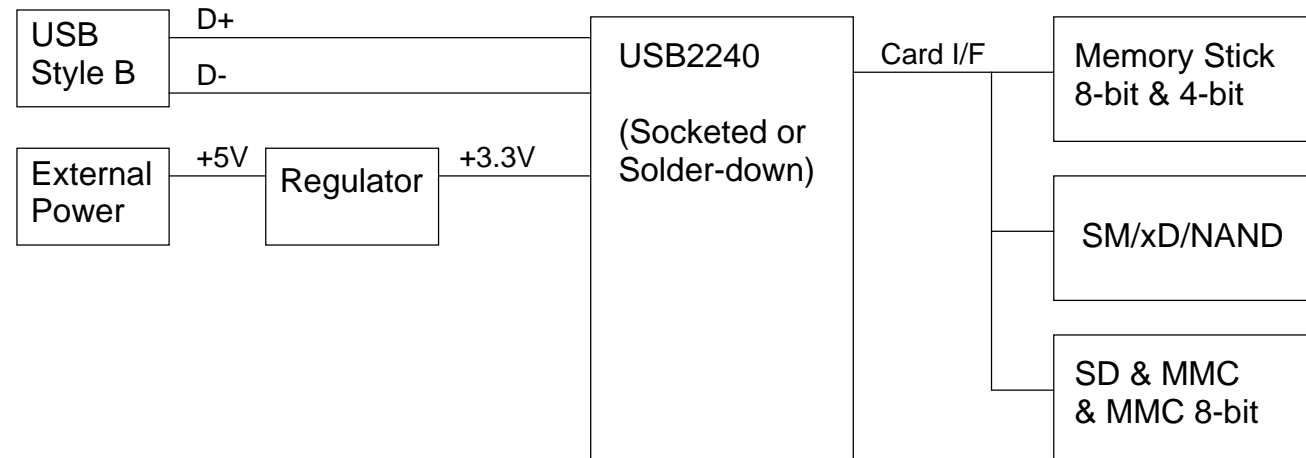


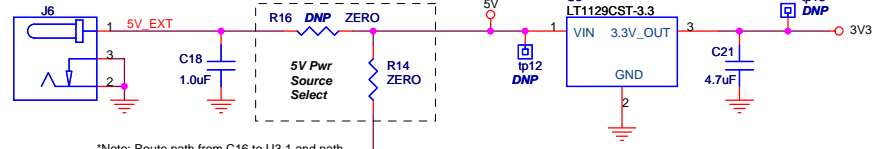
EVB-USB2240-IND

Evaluation Board for USB2240, 36 pin QFN - REFERENCE DESIGN



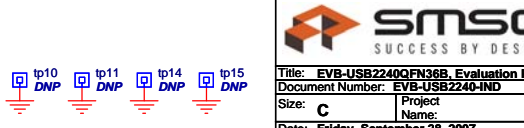
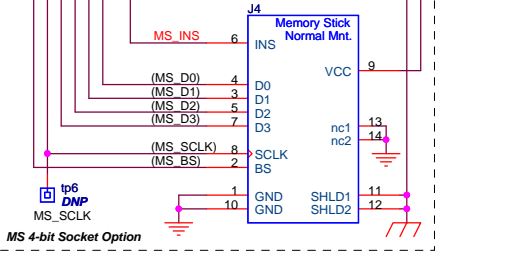
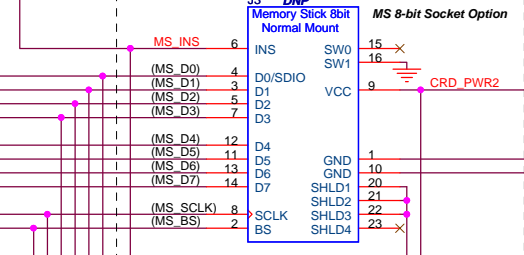
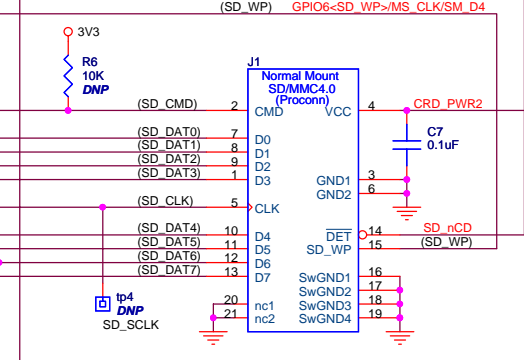
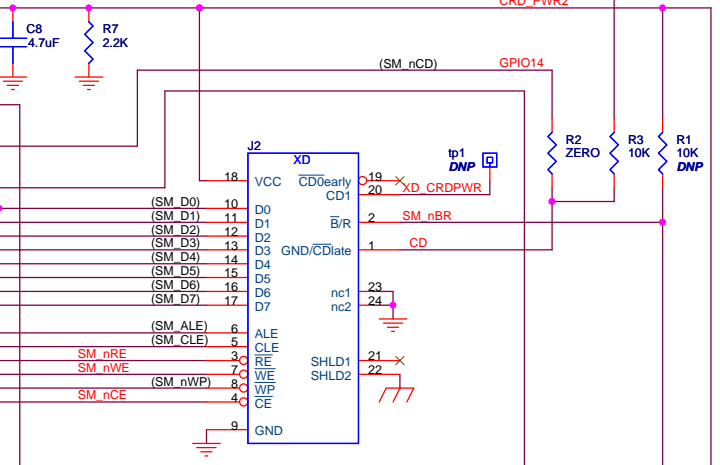
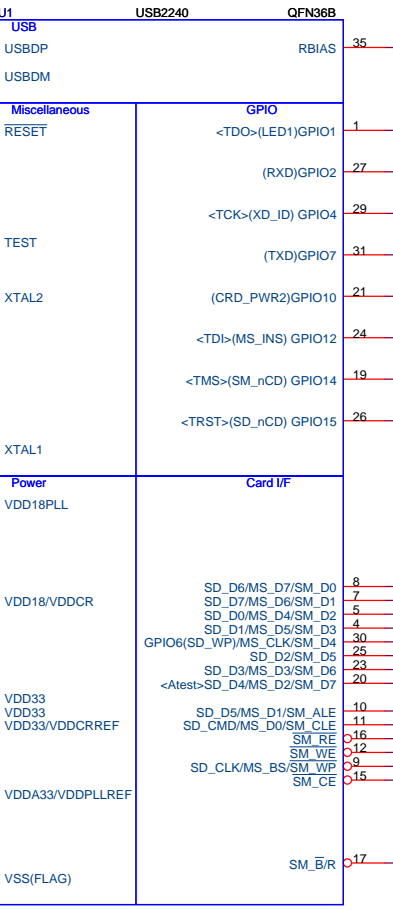
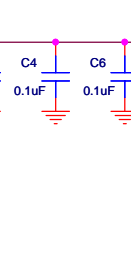
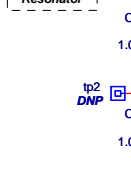
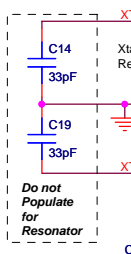
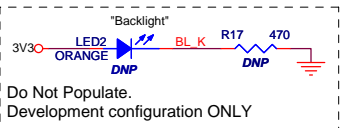
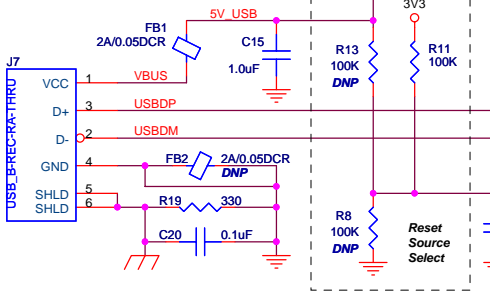
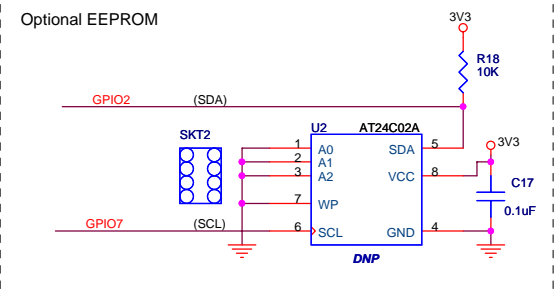
Revision History

Revision	Date	Revision Summary	Author
0.1	20070727	Initial design - based on SVB-USB2240QFN36B (Thamyris), Rev. 01	C. Johnson
0.2	20070730	Moved 1.0uF cap to far side of 5V selector to reduce in-rush current.	C. Johnson
0.3	20070828	Added optional EEPROM for configuration.	H. Magnusson
0.4	20070917	Added VBUS detect option for self-powered mode.	H. Magnusson
0.5	20070924	Revised DNP options, replaced 5V source jumper with resistors, replaced SM socket with xD socket, replaced 0805s w/ 0603, updated MMC socket.	C. Johnson
0.6	20070925	Renumbered.	C. Johnson
0.7	20070926	Changes to support new layout.	C. Johnson
A	20070928	Renumbered -- Released.	C. Johnson



*Note: Route path from C16 to U3.1 and path from C21 to U3.1 must be low inductance.

Buspowered mode: (Default) populate R4, R66, de-pop R3, R64, and R65.
Selfpowered mode: populate R3, R64, and R65, de-pop R4, and R66.



11000 N. Mopac Expressway
Stonlake Building 6, Suite 500
Austin, TX 78759-5428
512.502.0070

SMSC
SUCCESS BY DESIGN

Title: EVB-USB2240QFN36B, Evaluation Platform
Document Number: EVB-USB2240-IND
Size: C Project Name: Lyncus
Date: Friday, September 28, 2007 Rev A
Sheet 2 of 2