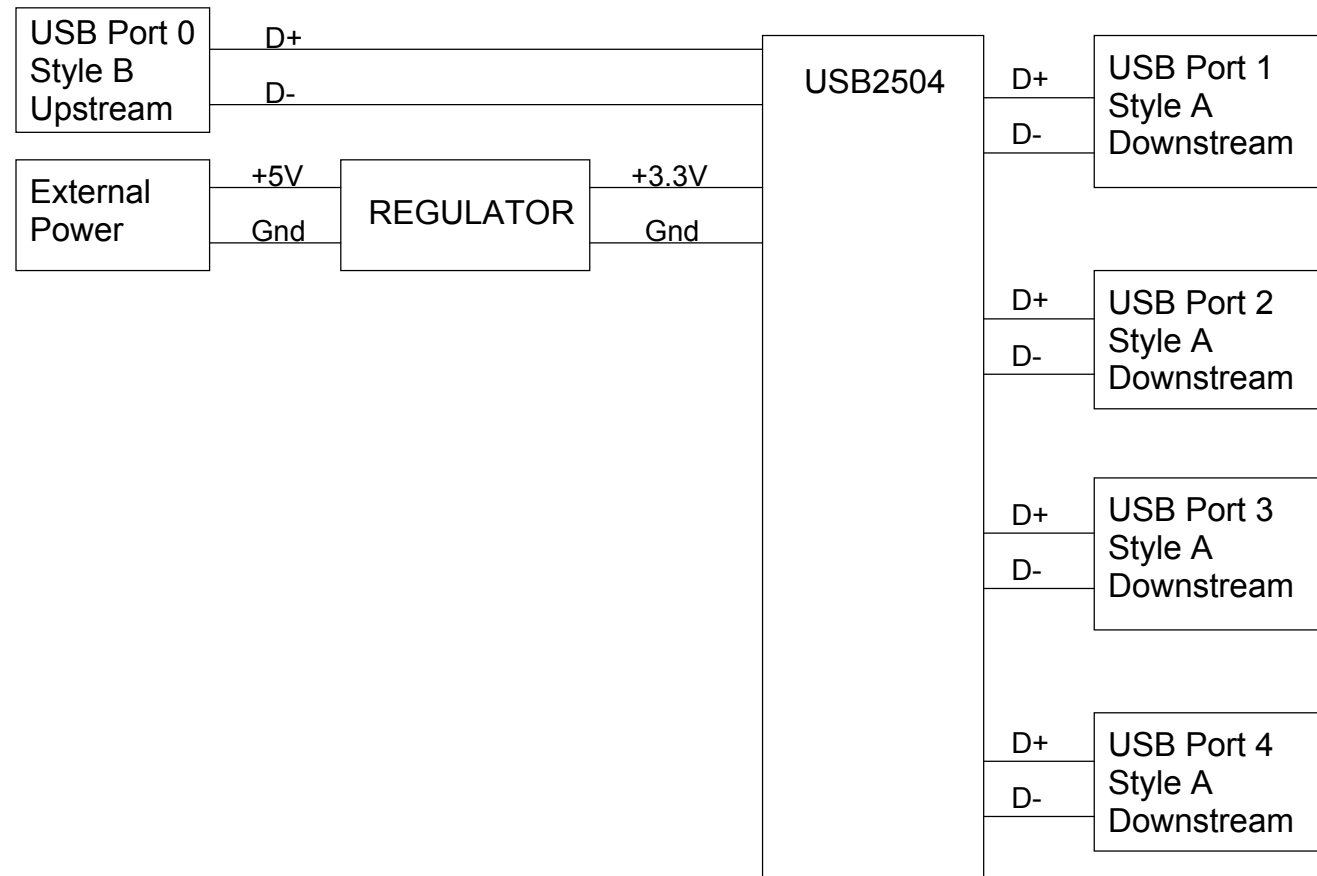


EVB-USB2504Q-LC

Evaluation Board for USB2504, 64-pin QFP, 2 Layers

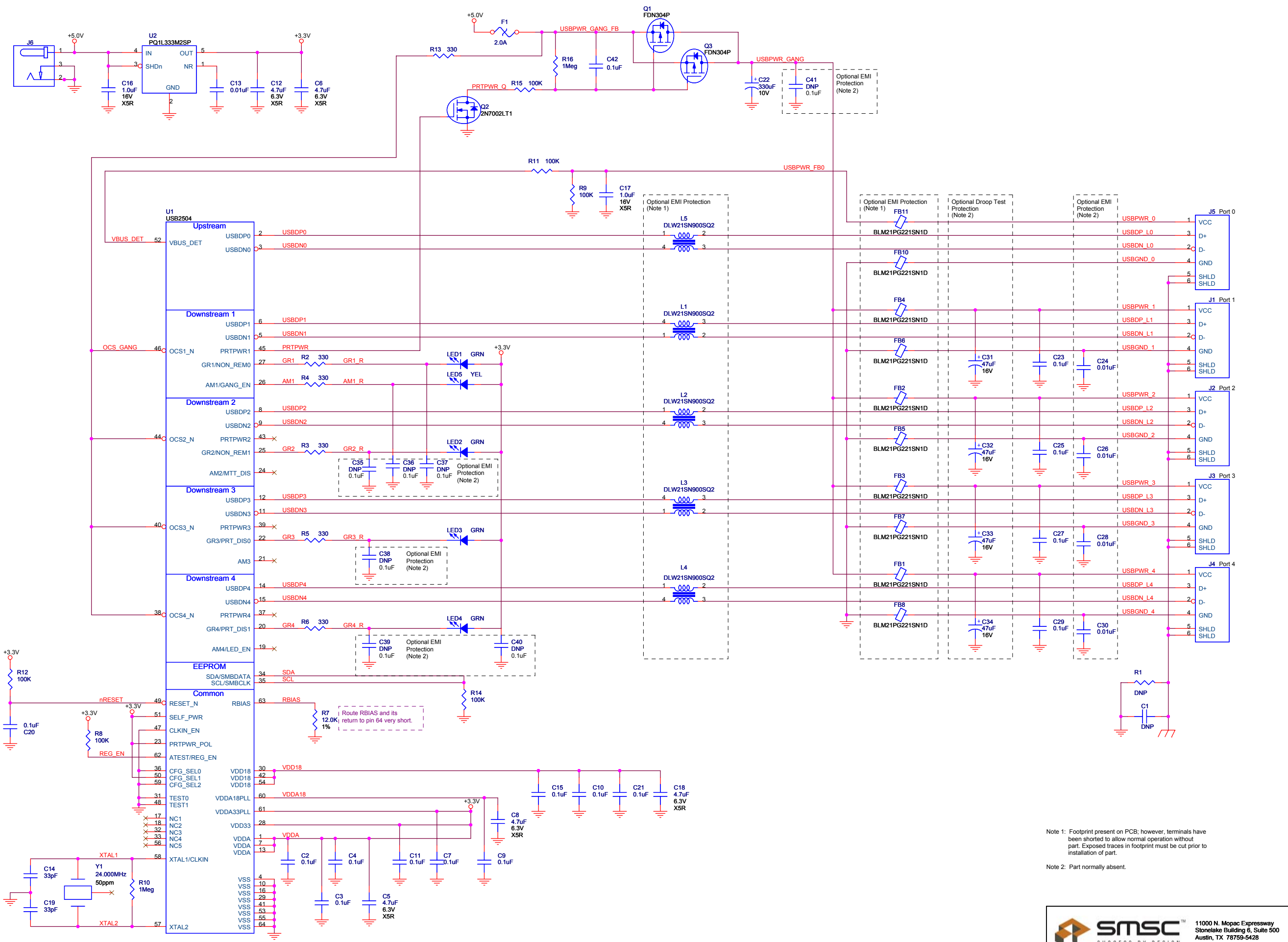


Revision History

Revision	Date	Revision Summary	Author
0.1-0.5	09/01/2004	Initial schematic	Edward Tatom
0.6	09/13/2004	Added title page, block diagram.	Carl Johnson
0.7	09/15/2004	Added caps to USB power pins, renumbered.	Carl Johnson
0.8	09/17/2004	Changed all 10uF caps to 4.7uF for cost reduction, added port-power control ckt, added EMI caps on LED lines, simplified the termination fo the EEPROM signals, changed the time constant of the reset ckt, standardized on 100K for pu and pd resistor values, removed FB9, standardized on 0.1uF caps for VDD bypass, changed VBUSDET ckt to improve ESD isolation, changed the USBPWR_GANG cap to 330uF to help pass droop test, removed the varistors on USB power and ground for all ports, identified the 47uF caps on ports 1-4 as DNP.	Carl Johnson
A	09/17/2004	Released	Carl Johnson
A1	10/13/2004	Change pick point of OCS_GANG, renumber LEDs, replaced obsolete regulator, changed values of VBUS_DET resistors.	Carl Johnson
A2	11/04/2004	Remove ESD protection diodes.	Carl Johnson



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Note 1: Footprint present on PCB; however, terminals have been shorted to allow normal operation without part. Exposed traces in footprint must be cut prior to installation of part.

Note 2: Part normally absent.



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