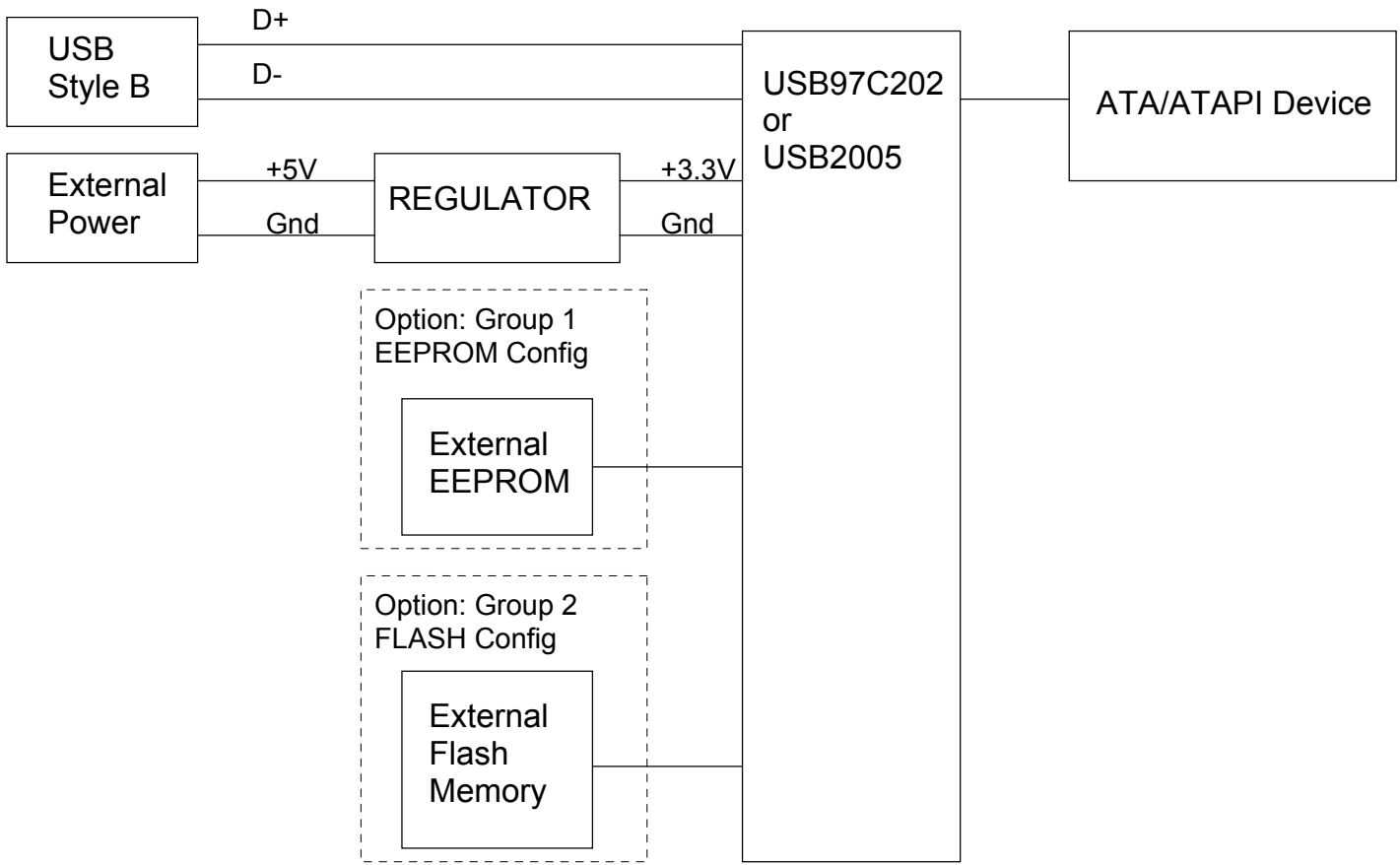


EVB-USB97C202

Evaluation Board for USB97C202 and USB2005, 2 Layers

Revision History

Revision	Date	Revision Summary	Author
A	09/09/2003	Initial design in four layers.	A. Tarascio
B	03/04/2005	Reduced BOM and build-cost. Two PCB layers, single side assembly. Bus-powered. (NOTE: ATA/ATAPI needs separate supply).	E. Tatom
B1	05/31/2005	Added reference to USB2005	H. Magnusson



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Title: EVB-USB97C202, USB97C202 and USB2005 Customer Reference Board, 2 Layer

Size C	Document Number EVB-USB97C202	Rev B
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Date: Tuesday, May 31, 2005 Sheet 1 of 2

